



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Confirmation No: 3840

#11/B
4/2/3
Sueles

Masaki KOMAKI

Art Unit: 2815

Serial No.: 09/939,752

Examiner: J. Nguyen

Filed: August 28, 2001

Docket No.: 024016-00014

For: FUNDAMENTAL CELL, SEMICONDUCTOR INTEGRATED DEVICE, WIRING METHOD AND WIRING APPARATUS

PRELIMINARY AMENDMENT WITH FILING OF RCE

Commissioner for Patents
Washington, D.C. 20231

March 25, 2003

Dear Sir:

Prior to continued examination of the application, please amend the above-identified application as follows:

IN THE CLAIMS:

Please amend and add the following claims:

1. (Twice Amended) A fundamental cell, used as a basic unit in layout of a semiconductor integrated circuit device, comprising:
B1 no fixed wiring for commonly wiring between fundamental cells in a hierarchy where fundamental cells are formed and connector terminals to be connected to upper wiring layers.

8. (Twice Amended) A semiconductor integrated circuit device, comprising:
B2 a fundamental cell, used as a basic unit in layout, having no fixed wiring to be commonly wired between the basic units in a hierarchy where fundamental

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